

# UNIT 2.

**FUNCTIONAL ELEMENTS OF A COMPUTER**

Activities-1

**Computer Systems**

**CFGS DAW**

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## Nomenclatura

A lo largo de este tema se utilizarán distintos símbolos para distinguir elementos importantes dentro del contenido. Estos símbolos son:

 Importante

 Atención

 Interesante

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### (Exercise 1)

Investigate the “Harvard modified architecture”.

You can obtain information in<https://en.wikipedia.org/wiki/Modified_Harvard_architecture>

*Post* the main differences with “Von Neumann architecture” in the forum and discuss them with your

classmates.

### (Exercise 2) 

Summarize how “Von Neumann architecture” works.

In this type of architecture, the incoming information is processed in the unit intended for this purpose.

Segment jobs in the logical arithmetic unit.

Once processed, it is sent to the switchboard.

Here are the instructions to memory them.

It is then sent to the output device.

All operations are performed on the same bus, causing limitations in system performance.

### (Exercise 3) 

Summarize the instruction cycle.

The instruction cycle is the time it takes for the CPU to execute the instructions received in machine code.

### (Exercise 4)

Try to find 2 samples of CISC CPUs and 2 samples of RISC CPUs. Share them in the forum and discuss them with your classmates.

### (Exercise 5)

Follow this tutorial <https://sites.google.com/site/kotukotuzimiti/>in order to understand how a 2 bit fictitious computer works. Share your solutions and ask your doubts using forum.